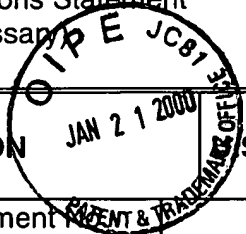


Form PTO-1449 (Modified)		Atty Docket No.: 042390.P7933		Serial No.: 09/458,570	
List of Patents and Publications Statement (Use several sheets if necessary) Page 1 of 3			Applicant: Dion Rodgers, et al. Filing Date: December 9, 1999		



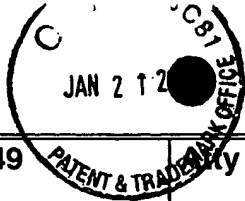
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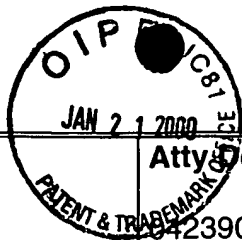
OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)		
RUE	AR	"Architectural and Implementation Tradeoffs in the Design of Multiple-Context Processors", James Laudon, Anoop Gupta and Mark Horowitz, <u>Multithreaded Computer Architecture: A Summary of the State of the Art</u> , chapter 8, pp. 167-200, Kluwer Academic Publishers 1994.
RUE	AS	"Exploiting Choice: Instruction Fetch and Issue on an Implementable Simultaneous Multithreading Processor", Dean M. Tullsen, Susan J. Eggers, Joel S. Emer, Henry M. Levy, Jack L. Lo and Rebecca L. Stammel, <u>Proceedings of the 23rd Annual International Symposium on Computer Architecture</u> , May 22-24, 1996, pp 191-202.
RUE	AT	"Evaluation of Multithreaded Uniprocessors for Commercial Application Environments", Richard J. Eickemeyer, Ross E. Johnson, Steven R. Kunkel, Mark S. Squillante and Shiafun Liu, <u>Proceedings of the 23rd Annual International Symposium on Computer Architecture</u> , May 22-24, 1996, pp 203-212

Examiner <u>Richard Ellis</u>	Date Considered <u>8/11/2003</u>
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

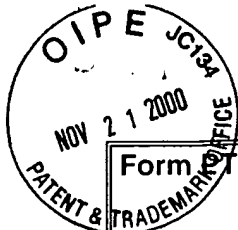


Form PTO-1449 (Modified)		Patent Docket No.: 042390.P7933		Serial No.: 09/458,570			
List of Patents and Publications Statement (Use several sheets if necessary) Page 2 of 3				Applicant: Dion Rodgers, et al.			
				Filing Date: December 9, 1999			
REFERENCE DESIGNATION			U.S. PATENT DOCUMENTS				
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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)							
	AR	"Performance Study of a Multithreaded Superscalar Microprocessor", Manu Gulati and Nader Bagherzadeh, <u>Proceedings Second International Symposium on High-Performance Computer Architecture</u> , February 3-7, 1996, pp 291-301.					
	AS	"A Benchmark Evaluation of a Multi-Threaded RISC Processor Architecture", R. Guru Prasad and Chuan-lin Wu, <u>1991 International Conference on Parallel Processing</u> , pp 1-84-191.					
	AT	"Multithreading Comes of Age", Peter Song, <u>Microdesign Resources</u> , July 14, 1997, pp 13-18.					
Examiner			Date Considered 8/11/2003				
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							



Form PTO-1449 (Modified)		Atty Docket No.: 09/42390.P7933		Serial No.: 09/458,570			
List of Patents and Publications Statement (Use several sheets if necessary) Page 3 of 3				Applicant: Dion Rodgers, et al.			
				Filing Date: December 9, 1999			
REFERENCE DESIGNATION			U.S. PATENT DOCUMENTS				
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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)							
RUE	AR	"Instruction Cache Fetch Policies for Speculative Execution", Dennis Lee, Jean-Loup Baer, Brad Calder and Dirk Grunwald, 22 nd International Symposium on Computer Architecture, June 1995.					
RUE	AS	"Multithreading for Rookies", Ruediger R. Asche, http://www.microsoft.com/win32dev/base/threads.htm , July 31, 1998.					
RUE	AT	Simon W. Moore, <u>Multithreaded Processor Design</u> , Kluwer Academic Publishers, 1996.					
Examiner- <u>Richard Ellis</u>			Date Considered <u>8/11/2003</u>				
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Form PTO-1449 (Modified)		Atty Docket No.: 042390.P7933		Serial No.: 09/458,570			
List of Patents and Publications Statement (Use several sheets if necessary) Page 1 of 1				Applicant: Dion Rodgers, et al. Filing Date: December 9, 1999			
REFERENCE DESIGNATION			U.S. PATENT DOCUMENTS				
Examiner Initials	Document No.	Document No.	Inventor	Class	Sub- Class	Filing date if appropriate	
<div style="transform: rotate(-90deg); font-weight: bold;"> RLE RUE </div>	AA	5,524,263	Griffth, et al.	395	800	Feb 25, 1994	
	AB	5,809,271	Colwell, et al.	395	384	Aug 23, 1995	
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Examiner <u>Richard Ellis</u>			Date Considered <u>8/11/2003</u>				
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Form PTO-1449 (Modified)		Atty Docket No.:		Serial No.:			
		042390.P7933		09/458,570			
List of Patents and Publications Statement (Use several sheets if necessary) Page 1 of 1				Applicant: Dion Rodgers, et al.			
				Filing Date: December 9, 1999			
REFERENCE DESIGNATION			U.S. PATENT DOCUMENTS				
Examiner Initials		Document No.		Class	Sub- Class	Filing date if appropriate	
<i>RUE</i>	AA	5,430,850	Papadopoulos, et al.	395	375	Jul 22, 1991	
<i>RUE</i>	AB	5,499,349	Nikhil, et al.	395	375	Oct 3, 1994	
<i>RUE</i>	AC	6,105,127	Kimura, et al.	712	215	Aug 27, 1997	
<i>RUE</i>	AD	5,404,469	Chung, et al.	395	375	Feb 25, 1992	
<i>RUE</i>	AE	6,092,175	Levy, et al.	712	23	Apr 2, 1998	
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Examiner <i>Richard Ellis</i>			Date Considered <i>8/11/2003</i>				
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

<p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)</p>	Application Number	09/458,570
	Filing Date	December 9, 1999
	First Named Inventor	Dion RODGERS
	Group Art Unit	2784
	Examiner Name	Not Yet Assigned
Sheet 1 of 2	Attorney Docket Number	042390.P7933

U.S. PATENT DOCUMENTS

Examiner Initials *	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Filing Date if Appropriate
	Number				
RUE	5,761,522		Hisanga, et al.	6/2/1998	4/18/1996
RUE	5,996,085		Cheong, et al.	11/30/1999	7/15/1997
RUE	5,999,932		Paul	12/7/1999	1/13/1998
RUE	6,052,709		Paul	4/18/2000	12/23/1997
RUE	6,115,709		Gilmour, et al.	9/5/2000	9/18/1998

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Translation? Yes/No
	Office or Country	Number	Date			
RUE	PCT/WHO	WO 99/21082	—	IBM Corporation	4/29/1999	No
RUE	European EPO	0 352 935	—	Internation Computers Limited	1/31/1990	No
RUE	European EPO	0 346 003	—	Fujitsu Limited	12/13/1989	No

OTHER DOCUMENTS

Examiner Initials *	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published (if known).	Translation? Yes/No

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Signature

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8/11/2003

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				Application Number		09/458,570	
				Filing Date		December 9, 1999	
				First Named Inventor		Dion RODGERS	
				Group Art Unit		2784	
				Examiner Name		Not Yet Assigned	
Sheet	2	of	2	Attorney Docket Number		042390.P7933	

U.S. PATENT DOCUMENTS					
Examiner Initials *		U.S. Patent Document	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Filing Date if Appropriate
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FOREIGN PATENT DOCUMENTS						
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RUE	European EPO	0 747 816	—	IBM	12/11/1996	No
RUE	European EPO	0 725 335	—	IBM	8/7/1996	No
RUE	European EPO	0 827 071	—	Matsushita Electric Industrial Co.	3/4/1998	No

OTHER DOCUMENTS		
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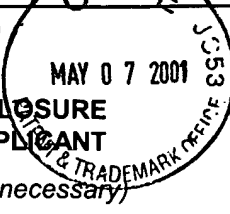
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				Application Number 09/458,570	
				Filing Date December 9, 1999	
				First Named Inventor Dion RODGERS	
				Group Art Unit 2784	
				Examiner Name Not Yet Assigned	
Attorney Docket Number 042390.P7933					
Sheet 1 of 2					

U.S. PATENT DOCUMENTS				
Examiner Initials *	U.S. Patent Document Number	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Filing Date if Appropriate
RLE	3,771,138	Celtruda, et al.	11/6/1973	8/31/1971
RLE	5,386,561	Huynh, et al.	1/31/1995	3/31/1992
RLE	5,968,160	Saito, et al.	10/19/1999	9/4/1997

FOREIGN PATENT DOCUMENTS						
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RLE	European EPD	0 962 856 A2	5.28.1999	Texas Instruments Inc.	12.8.1999	

OTHER DOCUMENTS		
Examiner Initials *	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published (if known).	Translation? Yes/No
RLE	STEERE D et al., "A Feedback-driven Proportion Allocator for Real-Rate Scheduling", <u>Third Symposium on operating Systems Design and Implementations</u> , February 22-25 1999, Pages 145-158, XP002153159	
RLE	INTEL, "P6 Family of Processors", <u>Hardware Developer's Manual</u> , September 1998, XP-002153160	
RLE	IBM, "Improves Dispatching in a Rendering Context Manager", <u>IBM Technical Disclosure Bulletin</u> , December 1990, Pages 131-134, Vol. 33, No. 7, XP000108363 ISSN: 0018-8689, Armonk, NY	

Examiner Signature <u>Richard Ellis</u>	Date Considered <u>8/11/2003</u>
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Substitute for form 1449A/PTO (Modified by BSTZ 6/30/99) INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	09/458,570
				Filing Date	December 9, 1999
				First Named Inventor	Dion RODGERS
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				Examiner Name	Not Yet Assigned
				Attorney Docket Number	042390.P7933
Sheet	2	of	2		

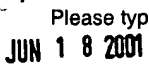
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Examiner Initials *		U.S. Patent Document Number	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Filing Date if Appropriate

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OTHER DOCUMENTS		
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RLE	FARRENS, MK; PLESZKUN, AR., "Strategies for Achieving Improved Processor Throughput", <u>The 18th Annual International Symposium on Computer Architecture</u> , May 27-30, 1991, Pages 362-369	
RLE	MENDELSON, A; BEKERMAN, M., "Design Alternatives of Multithreaded Architecture", <u>International Journal of Parallel Programming</u> , December 9, 1996, Vol. 27, No. 3, Pages 161-193, Pulenum Publishing Corporation	
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Examiner Signature <u>Richard Ellis</u>	Date Considered <u>8/11/2003</u>
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Substitute for form 1449A/PTO (Revised by BSTZ 6/30/99) INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				<i>Complete if Known</i>	
				Application Number	09/458,570
				Filing Date	December 9, 1999
				First Named Inventor	Dion ROGERS, et al.
				Group Art Unit	2784
				Examiner Name	Not yet assigned
Sheet	1	of	1	Attorney Docket Number	042390.P7933

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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Translation? Yes/No
	Office or Country	Number	Date			
RUE	European EPO	EP0747816	2	International Business Machines Corp.	12/11/1996	No
RUE	PCT WIPO	WO99/21088	2	International Business Machines Corp.	04/29/1999	No

OTHER DOCUMENTS

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RLE	PCT Written Opinion, 4/26/2001, PCT/US00/10800	No

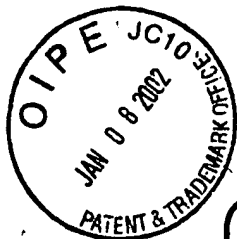
Examiner Signature	Richard Ellis	Date Considered	8/11/2003
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Form PTO-1446 (Modified) List of Patents and Publications Statement (Use several sheets if necessary)				Atty Docket No.: 042390.P7933		Serial No.: 09/458,570	
				Applicants: Dion Rodgers, et al.			
				Filing Date: December 9, 1999			
REFERENCE DESIGNATION				U.S. PATENT DOCUMENTS			
Exam. Initial	Date	Document Number	Name	Class	Sub- Class	Filing Date	
RUE	1	11/1/94	5361337	Okin	—	—	—
RUE	2	2/21/95	5392437	Matter, et al.	—	—	—
RUE	3	12/17/96	5586332	Jain, et al.	—	—	—
RUE	4	5/13/97	5630,130	Perotto, et al.	—	—	—
RUE	5	7/28/98	5787297	Lin	—	—	—
RUE	6	4/6/99	5892959	Fung	—	—	—
RUE	7	11/9/99	5983339	Klim	—	—	—
RUE	8	4/18/00	6052708	Flynn, et al.	—	—	—
RUE	9	7/11/00	6088788	Borkenhagen, et al.	—	—	—
RUE	10	4/3/01	US6212544 B1	Borkenhagen, et al.	—	—	—
RUE	11	7/3/01	US6256775 B1	Flynn	—	—	—
FOREIGN PATENT DOCUMENTS							
No.	Document No.	Date	Country Name	Class	Sub- Class	Trans- lation	
RUE	EP0864960A1	13-03-1998	European EPO	G06F 1/26	—		
	PCT/US00/28422	12-10-2000	PCT WIPO		—		
OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)							
RUE	DEAN M. TULLSEN, et al. "Exploiting Choice: Instruction Fetch and Issue on an Implementable Simultaneous Multithreading Processor" Dept. of Computer Science & Engineering, University of WA, Seattle, WA, <u>no date provided</u>						
RUE	ROA P. POKALA, et al., "Physical Synthesis for Performance Optimization", Vertex Semiconductor, San Jose, CA, <u>no date provided</u>						
RUE	GREGORY T. BYRD, et al., "MULTITHREADED Processor Architectures", Western Carolina University, 8045 IEEE Spectrum, 32(1995) August, No. 8, New York, US						
RUE	MARK R. THISTLE, et al., "A Processor Architecture For Horizon", Institute for Defense Analyses, Supercomputing Research Center, Lanham, Maryland 20706, <u>no date provided</u>						
Examiner <u>Richard Ellic</u>				Date Considered <u>8/11/2003</u>			
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 2

Complete if Known

Application Number	09/458,570
Filing Date	12/09/1999
First Named Inventor	Dion Rodgers
Art Unit	2784
Examiner Name	Not yet assigned
Attorney Docket Number	042390.P7933

U.S. PATENT DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
RLE	1	US- 5,809,522	09-15-1998	Novak et al.	—
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	3	US-			
	4	US-			
	5	US-			
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FOREIGN PATENT DOCUMENTS

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		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
RLE	21	EP	0 768 608	A2	04-16-1997	Sun Microsystems, Inc.		
RLE	22	EP	0 768 608	A3	04-16-1997	Sun Microsystems, Inc.		
RLE	23	EP	0 856 797	A1	08-05-1998	SGS-Thomson Microelectronics I.T.N.		
RLE	24	GB	2311880	A	10-08-1997	Advanced RISC Machines Limited		
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Signature

Richard Ellis

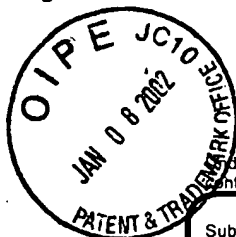
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/458,570
		Filing Date	12/09/1999
		First Named Inventor	Dion Rodgers
		Group Art Unit	2784
		Examiner Name	Not yet assigned
Attorney Docket Number	042390.P7933		
Sheet	2	of	2

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials [*]	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
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